

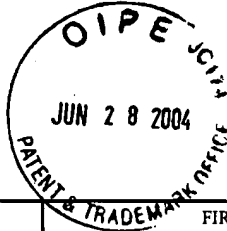
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22852 7590 03/29/2004

FINNEGAN, HENDERSON, FARABOW, GARRETT &
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LLP
1300 I STREET, NW
WASHINGTON, DC 20005

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/081,109	02/25/2002	Shinsuke Sakamoto	4329.2270-01	4805

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND WIRING ARRANGING METHOD THEREOF

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/29/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LEE, EUGENE	2815	257-776000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 FINNEGAN, HENDERSON,

2 FARABOW, GARRETT &

3 DUNNER, L.L.P.

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

KABUSHIKI KAISHA TOSHIBA

Kawasaki-shi, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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Reg. No. 25,961 (Date) JUN 28 2004

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01 FC:1501	1330.00 OP
02 FC:1504	300.00 OP
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